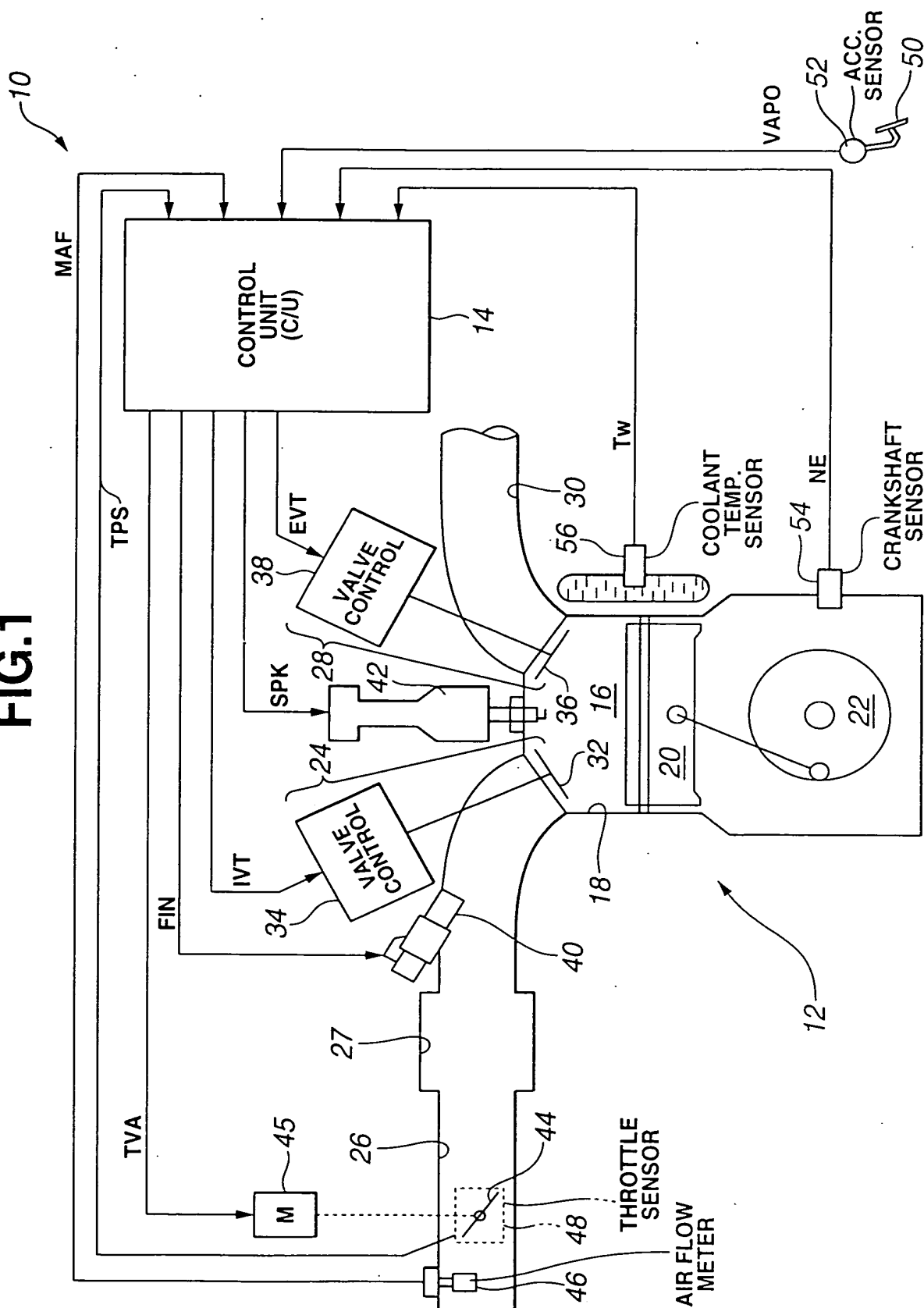


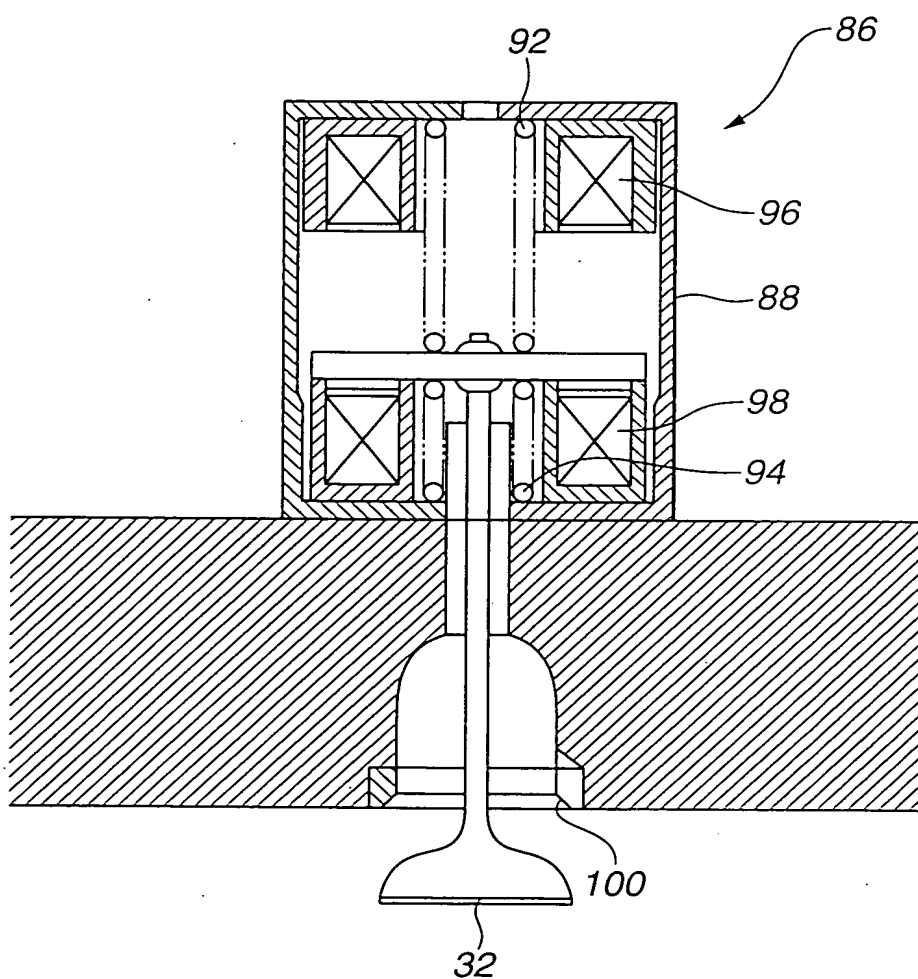
000001 1552260

FIG.1



APPROVED	O.C. FIG.
BY	CLASS/SUBCLASS
DRAFTSMAN	

**FIG.2**

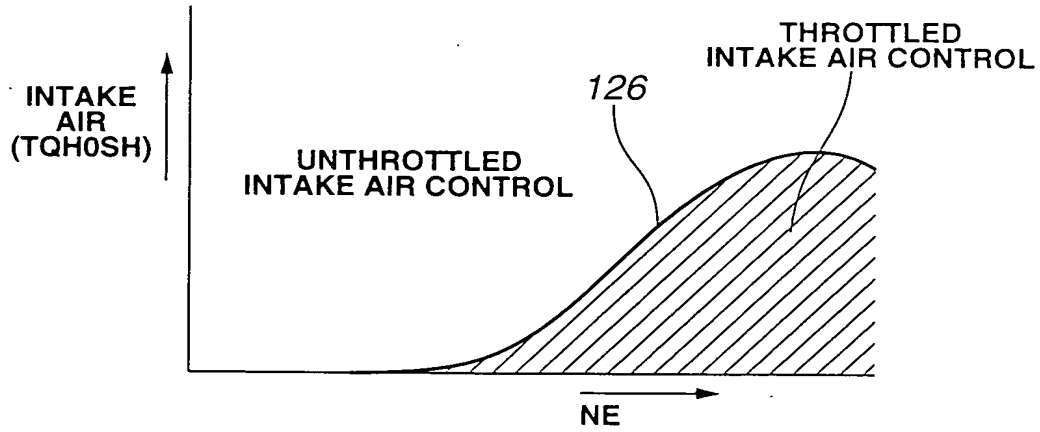


Block diagram of a control system 14. The system includes an MPU (106), ROM (114), RAM (112), and KAM (116) connected to a central bus (108). The bus is also connected to OUTPUT PORTS (118) and INPUT PORTS (104). The OUTPUT PORTS are connected to TVA, FIN, SPK, IVT, and EVT. The INPUT PORTS are connected to TPS, MAF, TW, VAPO, and NE(RPM).



APPROVED	O.C. FIG.	
BY	CLASS	SUBCLASS
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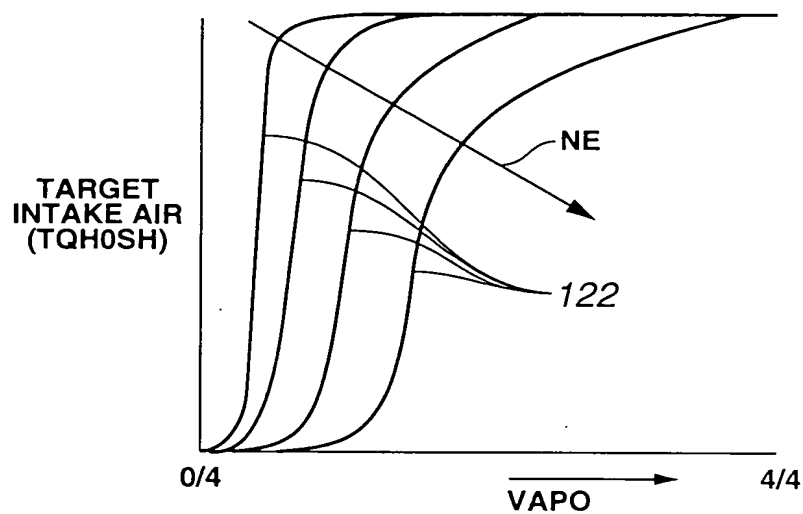
FIG.5



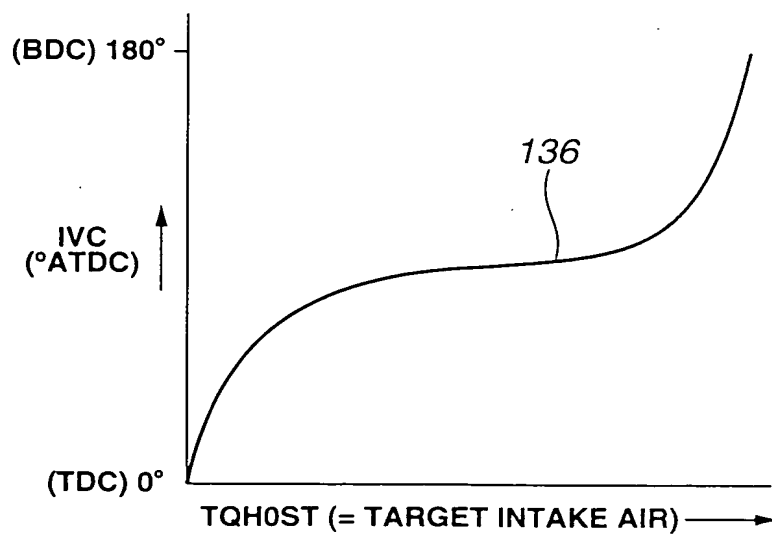
007254-120400

APPROVED	O.G. FIG.	
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**FIG.6**

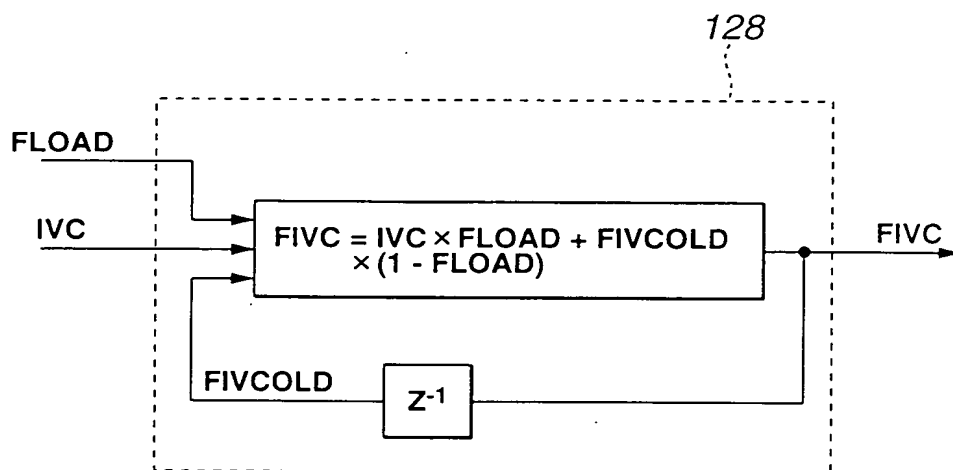


**FIG.7**

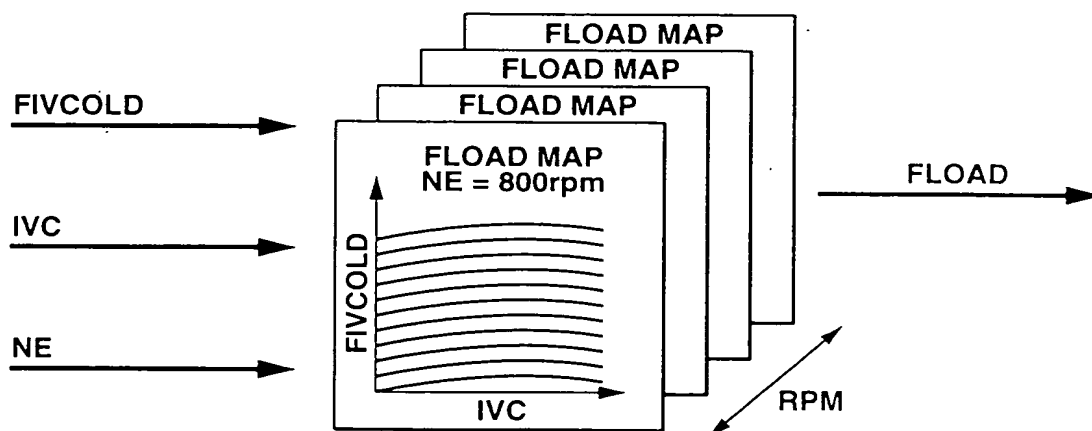


APPROVED	O.C. FIG.
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**FIG.8**



**FIG.9**



APPROVED	O.C. FIG.
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**FIG.10**

